

Amendments to the Claims

Listing of Claims:

5 Claim 1 (original): A method for refreshing at least a program code in an electronic system, the electronic system comprising a host device and a peripheral device, the peripheral device comprising:
a control circuit for executing a first program code to control operations of the peripheral device according to an instruction from the host device;
10 the method comprising:
accessing a second program code; and
executing an inspection step in the host device before the second program code replaces the first program code of the peripheral device to utilize the host device to check whether partial content of the second program code conforms
15 to a predetermined content.

Claim 2 (original): The method of claim 1 wherein the peripheral device further comprises a storage memory for non-volatilely storing the first program code; when the first program code is replaced by the second program code, the first
20 program code is erased from the storage memory, and the second program code is recorded into the storage memory.

Claim 3 (original): The method of claim 1 wherein when executing the inspection step in the host device before the second program code replaces the first program code,
25 the inspection step is proceeded before the control circuit executes the second program code to control operations of the peripheral device.

Claim 4 (original): The method of claim 1 wherein the predetermined content is partial

5 content of the first program code or a constant recorded in the first program code
and executing the inspection step in the host device comprises checking whether
the second program code includes partial content of the first program code, or
whether the constant recorded in the second program code is equal to the
constant in the first program code, or whether the constant recorded in the second
program code conforms to a predetermined range of the constant in the first
program code.

10 Claim 5 (original): The method of claim 1 wherein the predetermined content is a fixed
content so that the predetermined content cannot be changed when the second
program code is changed.

15 Claim 6 (original): The method of claim 1 wherein when executing the inspection step in
the host device, the host device will access partial content in a predetermined
address in the second program code to check whether the partial content
conforms to the predetermined content, or to search if the predetermined content
exists in the second program code.

20 Claim 7 (original): The method of claim 1 further comprising ceasing to replace the first
program code with the second program code after executing the inspection step
in the host device if partial content of the second program code does not conform
to the predetermined content.

25 Claim 8 (currently amended): The method of claim 1 further comprising replacing the
first program code with the second program code after executing the inspection
step in the host device so that the control circuit can execute the second program
code to control operations of the peripheral device if partial content of the second
program code conforms to the predetermined content[[]].

Claim 9 (original): A method for refreshing at least a program code in an electronic system, the electronic system comprising a host device and a peripheral device, the peripheral device comprising:

- 5 a control circuit for executing a first program code to control operations of the peripheral device;
the method comprising:
transmitting a second program code from the host device to the peripheral device;
and
10 executing a device inspection step, before the second program code replaces the first program code of the peripheral device, to utilize the control circuit to check whether partial content of the second program code conforms to a predetermined content.

- 15 Claim 10 (original): The method of claim 9 wherein the peripheral device further comprises a storage memory for non-volatilely storing the first program code; when the first program code is replaced by the second program code, the first program code is erased from the storage memory, and the second program code is recorded into the storage memory.

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Claim 11 (original): The method of claim 9 wherein when executing the device inspection step before the second program code replaces the first program code, the device inspection step precedes the control circuit executing the second program code to control operations of the peripheral device.

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Claim 12 (original): The method of claim 9 wherein the predetermined content is partial content of the first program code, or a constant recorded in the first program code and executing the device inspection step comprises checking whether the

5 second program code includes partial content of the first program code, or
checking whether the constant recorded in the second program code is equal to
the constant in the first program code, or checking whether the constant
recorded in the second program code conforms to a predetermined range of the
constant in the first program code.

10 Claim 13 (original): The method of claim 9 wherein the predetermined content is a fixed
content so that the predetermined content cannot be changed when the first
program code is replaced by the second program code.

15 Claim 14 (original): The method of claim 9 wherein when executing the device inspection
step, the control circuit accesses partial content in a predetermined address in
the second program code to check whether the partial content conforms to the
predetermined content, or to search if the predetermined content exists in the
second program code.

20 Claim 15 (original): The method of claim 9 further comprising ceasing to replace the first
program code with the second program code after executing the device
inspection step if partial content of the second program code does not conform
to the predetermined content.

25 Claim 16 (original): The method of claim 9 further comprising replacing the first program
code with the second program code after executing the device inspection step so
that the control circuit can execute the second program code to control
operations of the peripheral device if partial content of the second program code
conforms to the predetermined content.

 Claim 17 (original): The method of claim 9 wherein the peripheral device further

comprises a buffer for volatilely storing data; when executing the device inspection step, the control circuit temporally stores the second program code into the buffer to access partial content of the second program code and to proceed with the device inspection step.

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Claim 18 (original): The method of claim 17 wherein the peripheral device further comprises a non-volatile storage memory for non-volatilely storing the first program code; when executing the device inspection step before the first program code is replaced by the second program code, the device inspection step precedes the first program code being erased and the second program code being recorded in the non-volatile storage memory.

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Claim 19 (original): The method of claim 9 wherein the peripheral device is an optical drive.

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Claim 20 (original): A peripheral device comprising:

a control circuit for executing a first program code to control operations of the peripheral device; the control circuit comprising a checking module, the checking module being used to check whether partial content of the second program code conforms to a predetermined content before the control circuit replaces the first program code with a second program code.

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Claim 21 (original): The peripheral device of claim 20 further comprising a non-volatile storage memory for non-volatilely storing the first program code; when the first program code is replaced by the second program code, the first program code is erased from the non-volatile storage memory and the second program code is recorded into the non-volatile storage memory.

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5 Claim 22 (original): The peripheral device of claim 20 wherein when the checking module operates an examining process before the control circuit replaces the first program code with the second program code, the checking module operates before the control circuit executes the second program code to control operations of the peripheral device.

10 Claim 23 (original): The peripheral device of claim 20 wherein the predetermined content is partial content of the first program code, or a constant recorded in the first program code; when the checking module operates an examining process, the checking module checks whether the second program code includes partial content of the first program code, or whether a constant recorded in the second program code is equal to the constant in the first program code, or whether the constant recorded in the second program code conforms to a predetermined range of the constant in the first program code.

15 Claim 24 (original): The peripheral device of claim 20 wherein the predetermined content will not be changed when the first program code is replaced by the second program code.

20 Claim 25 (original): The peripheral device of claim 20 wherein when the checking module operates an examining process, the control circuit will access partial content in a predetermined address in the second program code so that the checking module can check whether the partial content conforms to the predetermined content, or to search if the predetermined content exists in the
25 second program code.

Claim 26 (original): The peripheral device of claim 20 wherein if partial content of the second program code does not conform to the predetermined content, the

control circuit will cease to replace the first program code with the second program code after the checking module operates an examining process.

5 Claim 27 (original): The peripheral device of claim 20 wherein after the checking module ensures that partial content of the second program code conforms to the predetermined content, the control circuit replaces the first program code with the second program code so that the control circuit can execute the second program code to control operations of the peripheral device.

10 Claim 28 (original): The peripheral device of claim 20 further comprising a buffer for volatily storing data, wherein the control circuit temporally stores the second program code in the buffer and the checking module operates an examining process after the control circuit accesses partial content of the second program code.

15 Claim 29 (original): The peripheral device of claim 28 being applied in an electronic system, the electronic system further comprising a host device, wherein the second program code is transmitted from the host device to the peripheral device.

20 Claim 30 (original): The peripheral device of claim 28 further comprising a non-volatile storage memory for non-volatily storing the first program code; when the checking module operates the examining process before the first program code is replaced by the second program code, the device inspection step precedes the
25 first program code being erased and the second program code being recorded in the non-volatile storage memory.

Claim 31 (original): A method for refreshing at least a program code in an electronic

system, the electronic system comprising a host device and a peripheral device,
the peripheral device comprising:

a control circuit for executing a first program code to control operations of the
peripheral device according to an instruction from the host device;

5 the method comprising:

accessing a second program code; and

executing an inspection step before the second program code replaces the first

program code of the peripheral device to generate a corresponding content

10 characteristic according to the second program code and to check whether the

corresponding content characteristic conforms to a predetermined

characteristic, the predetermined characteristic being not changed when the

first program code is replaced by the second program code.

15 Claim 32 (original): The method of claim 31 wherein the inspection step is proceeded by
the host device.

Claim 33 (original): The method of claim 31 wherein the inspection step is proceeded by
the control circuit of the peripheral device.

20 Claim 34 (original): The method of claim 31 wherein the content characteristic is an
address where a predetermined content is located in the second program code
and the predetermined characteristic is a predetermined address; the inspection
step further comprising checking whether the address where the predetermined
content is located in the second program code is equal to the predetermined
25 address.